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Docket No. BUR919990304US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Patent Application: Ference et al.

Group Art Unit: : IBM Corporation

Examiner: : 1000 River Street

Serial No.: : Essex Junction, VT 05452

Filed: Herewith

Title: MULTI-CHIP STACK AND METHOD OF FABRICATION
UTILIZING SELF-ALIGNING ELECTRICAL CONTACT ARRAY

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the publications identified below and listed on the enclosed PTO-1449 Form(s) be considered by the United States Patent and Trademark Office (USPTO) in the subject United States patent application and be made of record therein. A copy of each publication is enclosed, and this submission is otherwise believed to be in compliance with 37 C.F.R. Sections 1.97 and 1.98. This information is all the material information, as defined in 37 C.F.R. Section 1.56(a), of which the submitter is aware. However, no representation is made or intended that a search has been made or that no other such material information exists.

PATENTS

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|--------------------------|--------------|--------------|
| 1. 5,863,406 | 2. 5,567,295 | 3. 5,381,307 |
| 4. 4,182,781 | 5. 4,487,654 | 6. 5,226,232 |
| 7. 3,869,787 | 8. 4,032,058 | 9. 5,567,654 |
| 10. 05-190553 (Abstract) | | |

None of the above-cited publications, taken singularly or in combination, teach or suggest the invention as defined by the claims provided in the instant application.

Respectfully submitted,

Dated: November 28, 2000

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